

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Yoji Idei, et al.	Examiner:	Unassigned
Serial No:	Unassigned	Art Unit:	Unassigned
Filed:	Herewith	Docket:	17051
For:	SEMICONDUCTOR DEVICE USING CURRENT MIRROR CIRCUIT	Dated:	September 24, 2003

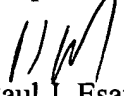
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CLAIM OF PRIORITY

Sir:

Applicants in the above-identified application hereby claim the right of priority in connection with Title 35 U.S.C. § 119 and in support thereof, will submit in due course a certified copy of Japanese Patent Application No. 2002-280855, filed on September 26, 2002.

Respectfully submitted,


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Dated: September 24, 2003

Paul J. Esatto, Jr.